

```
graph LR; 10[DATA STORAGE SYSTEM] --- 15[FILE SYSTEM]; 15 <--> 20[MEMORY DEVICE];
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The diagram illustrates the system architecture. It consists of three main components: a large box labeled "DATA STORAGE SYSTEM" (10), a smaller box labeled "FILE SYSTEM" (15) positioned below and to the right of the data storage system, and another box labeled "MEMORY DEVICE" (20) positioned to the right of the file system. A double-headed arrow connects the "FILE SYSTEM" (15) and the "MEMORY DEVICE" (20), indicating bidirectional communication between them.

Fig. 1

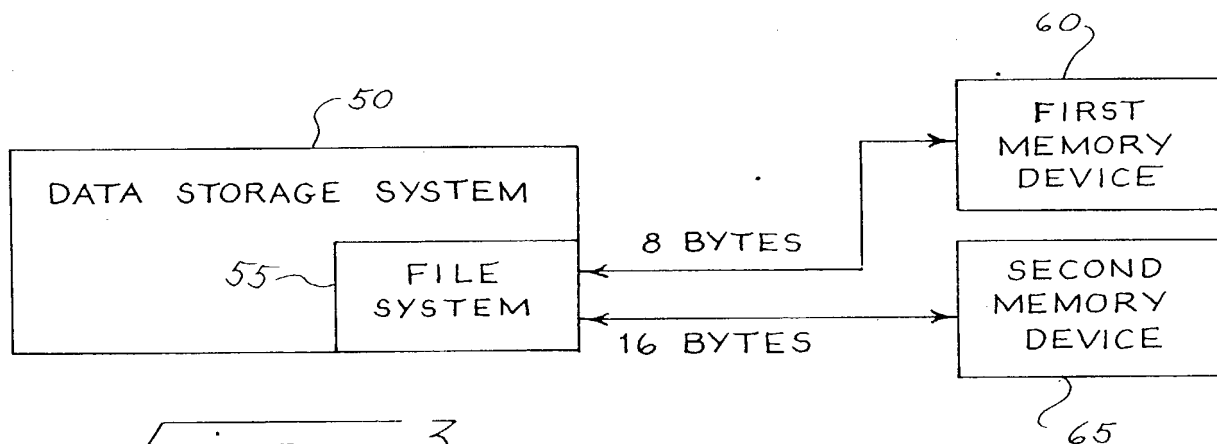
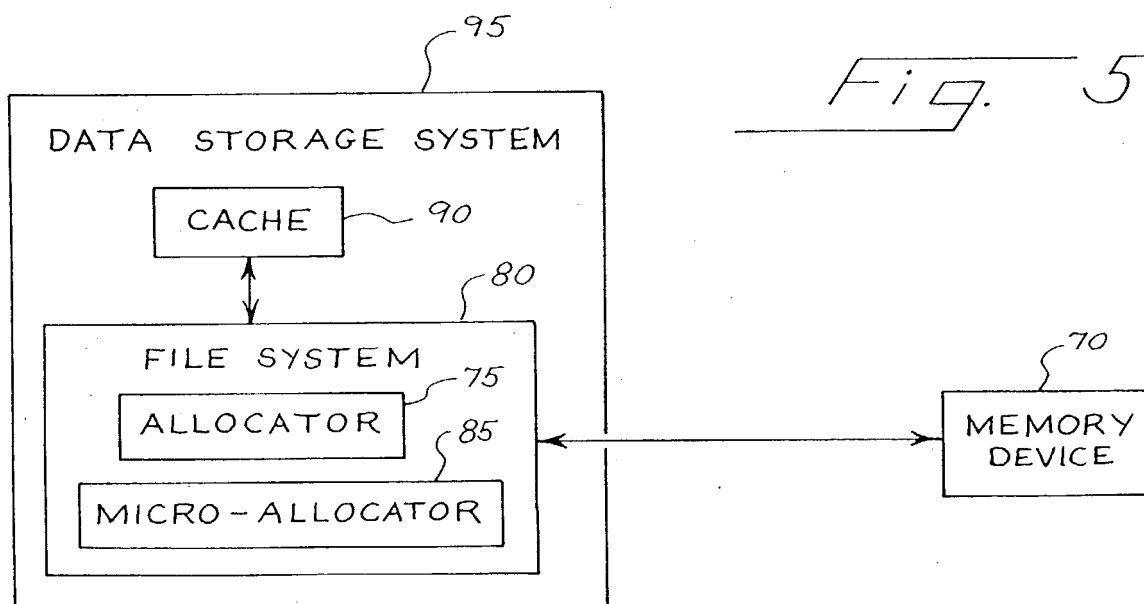


Fig. 3



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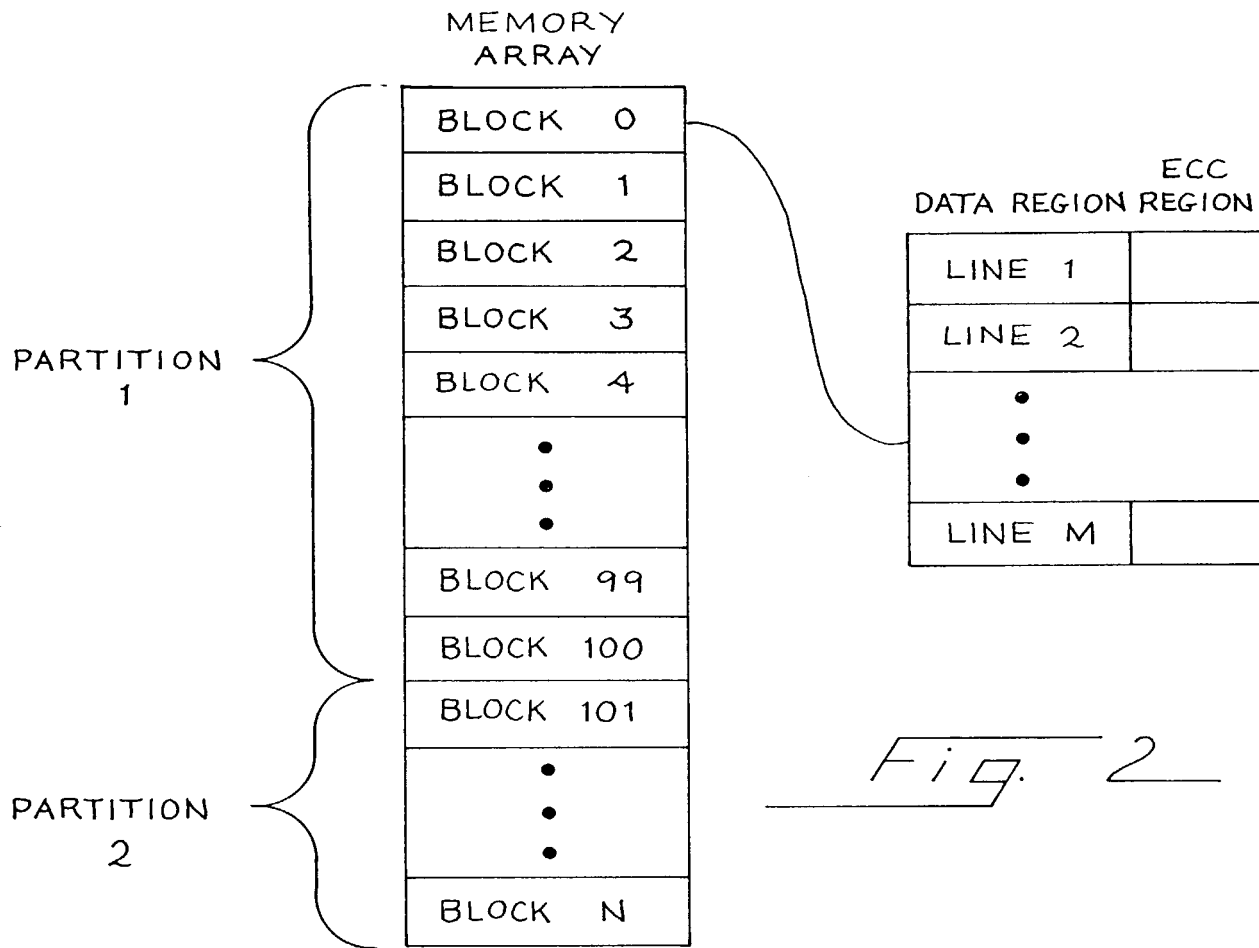


Fig. 2

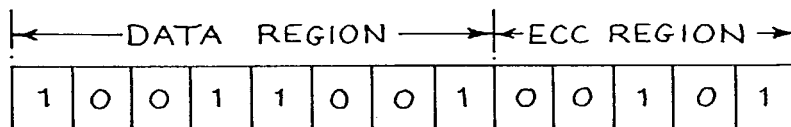


Fig. 4A

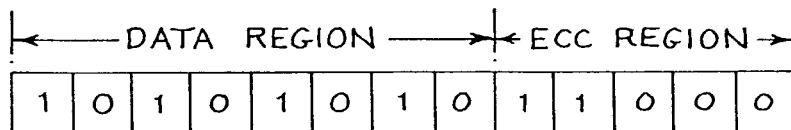


Fig. 4B

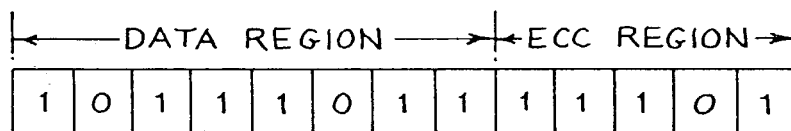
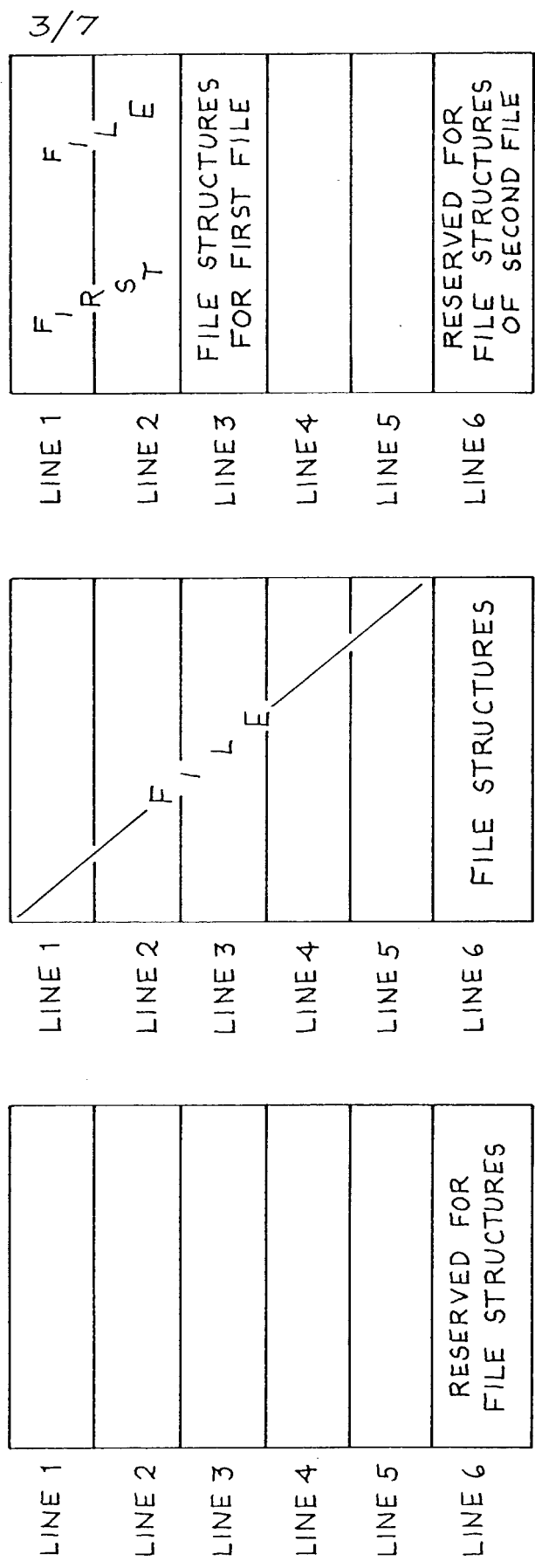
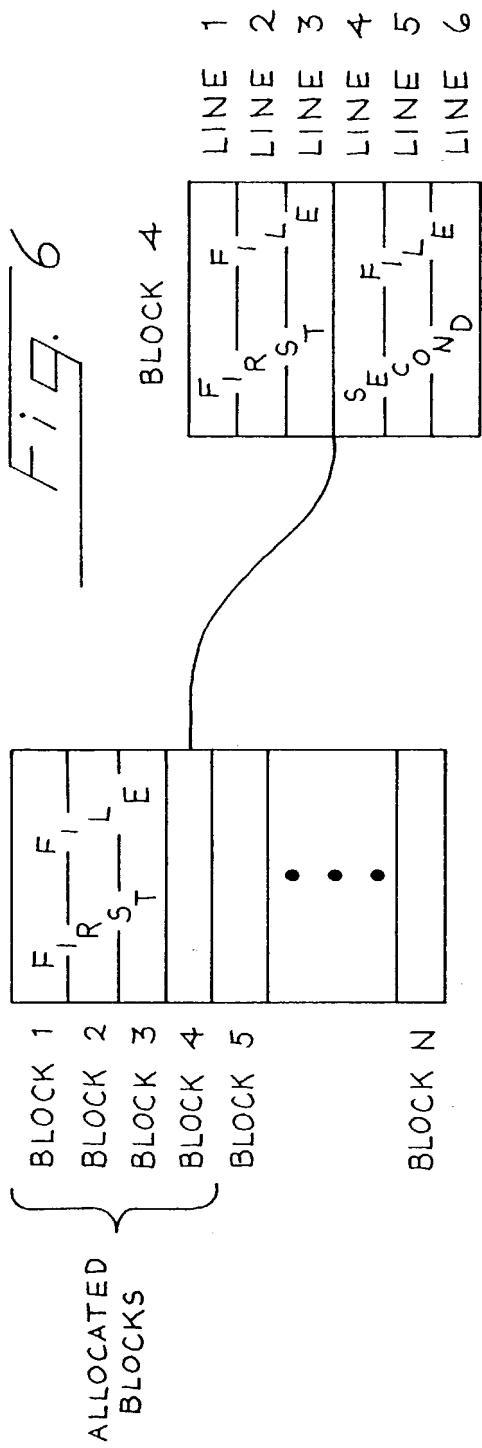


Fig. 4C

FIG. 6



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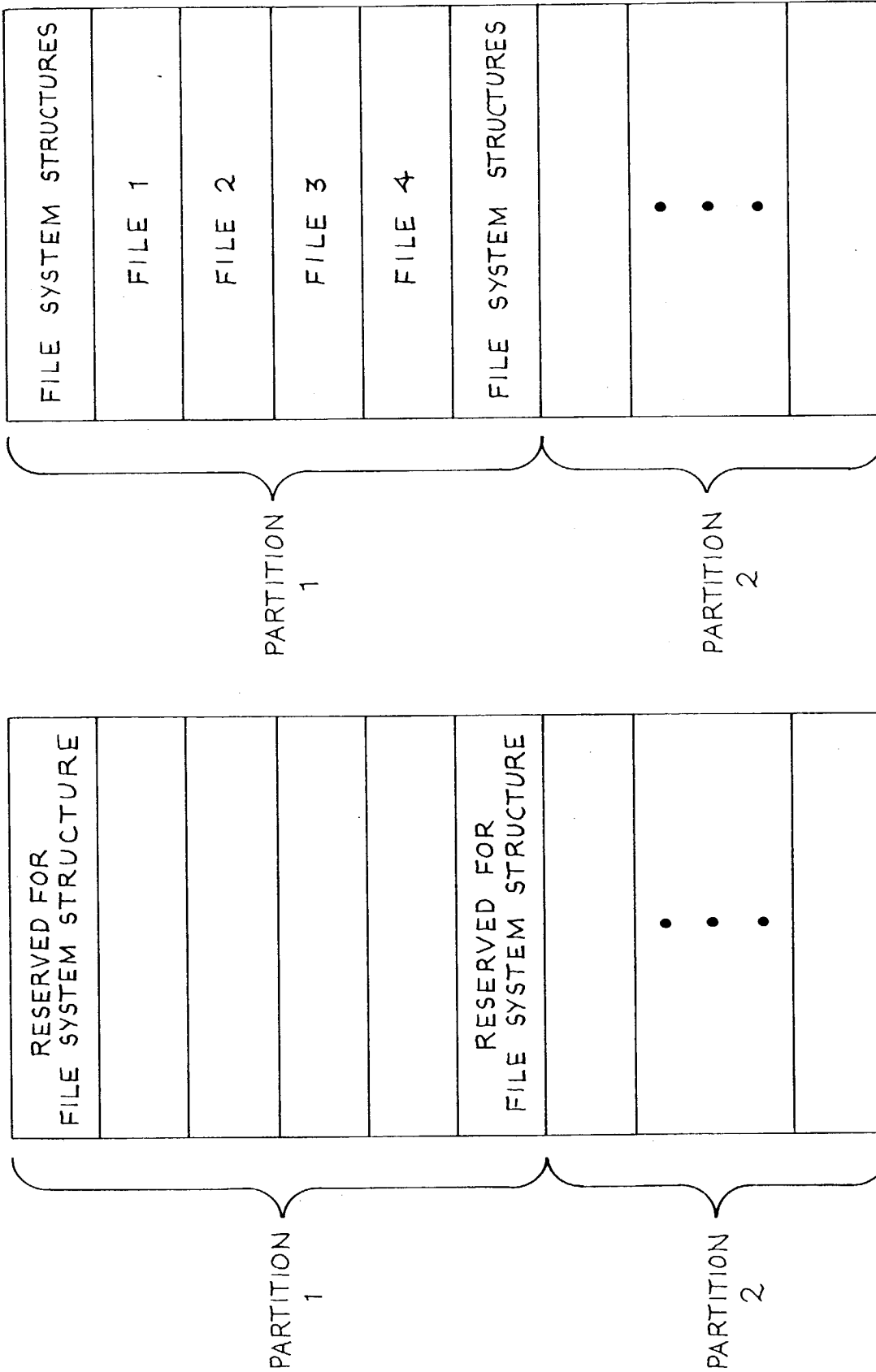


Fig. 8B

Fig. 8A

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FIG. 9A

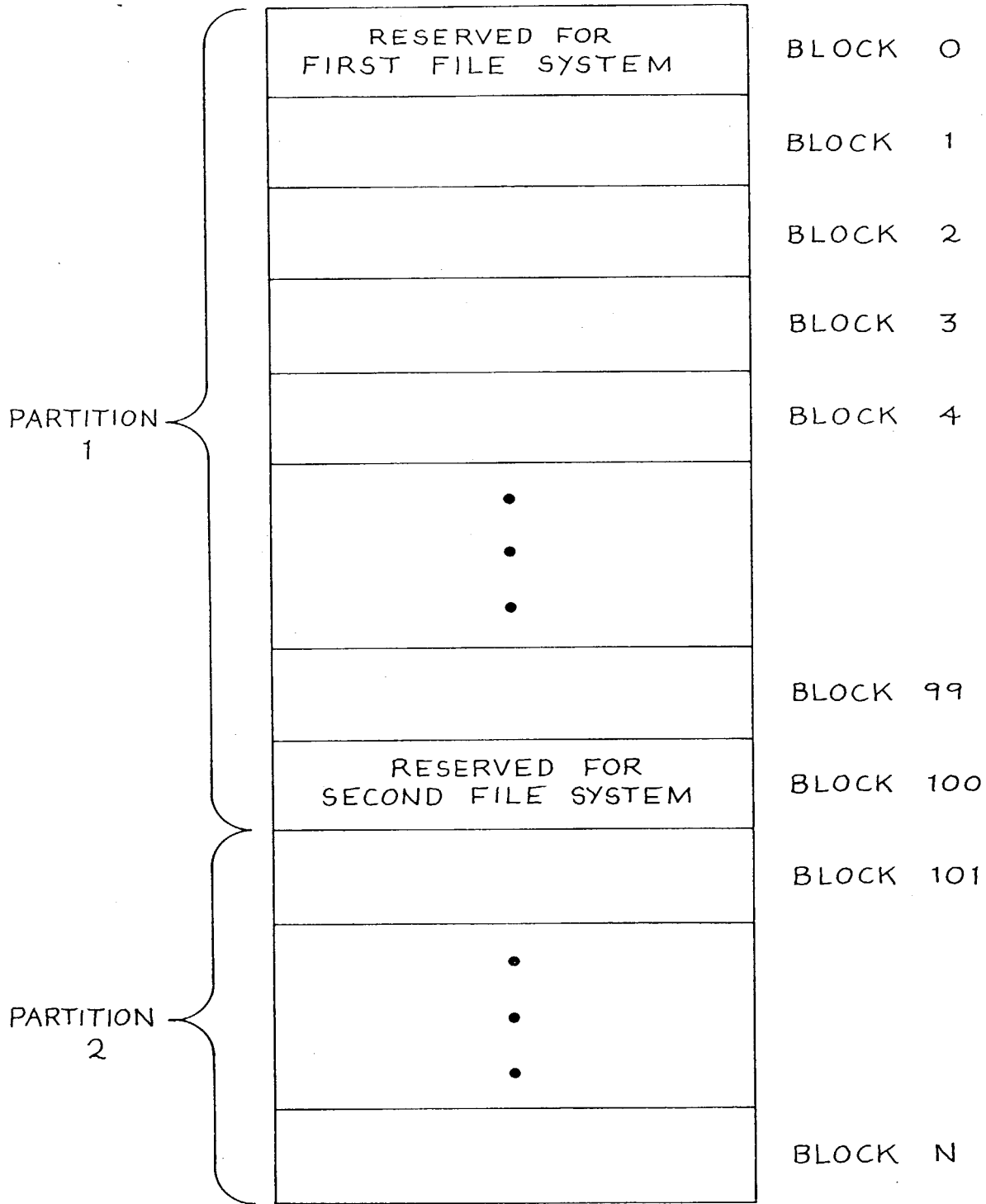


Fig. 9A

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FIG. 9B

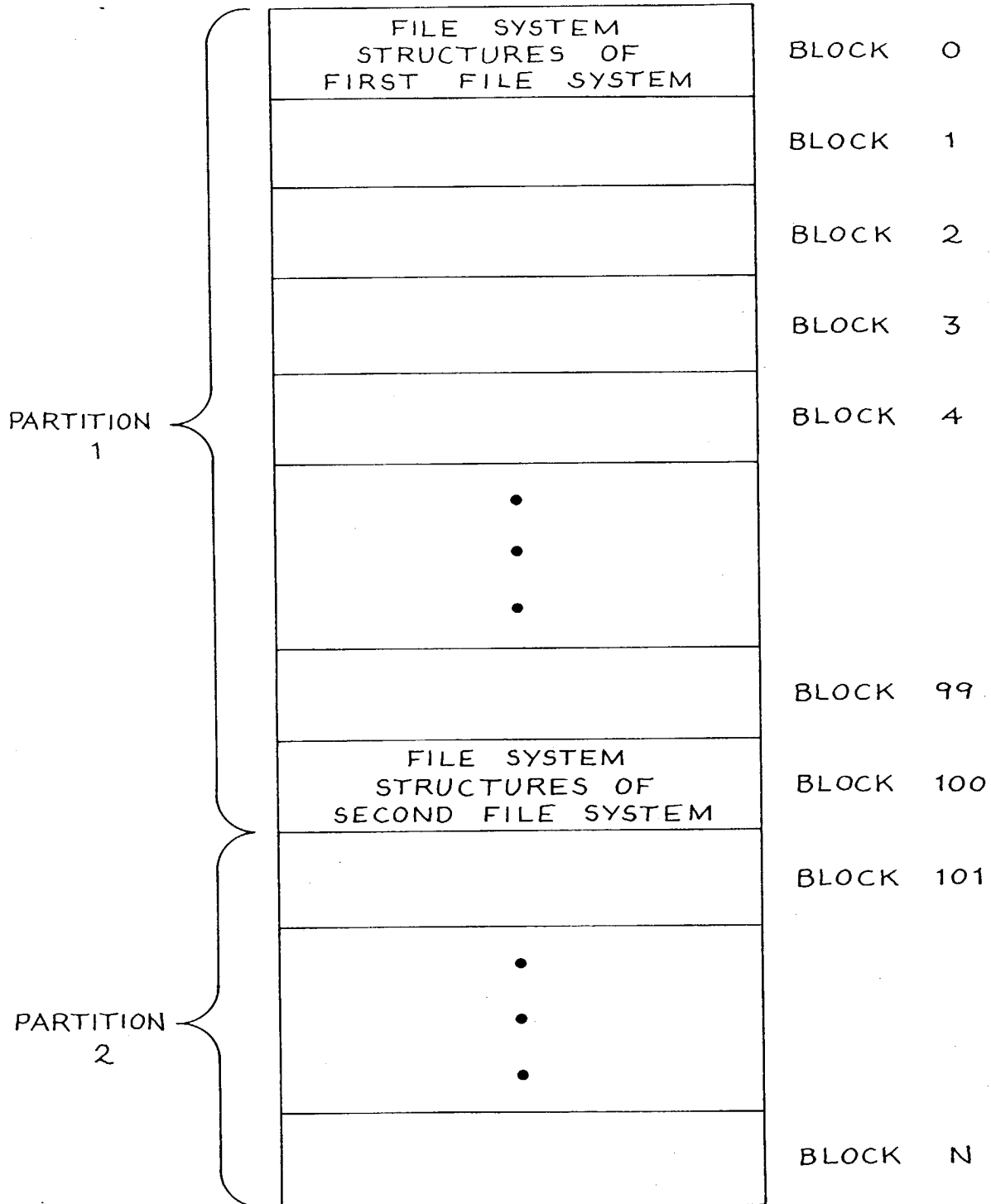


Fig. 9B

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1	1	0	1
0	0	0	0
0	0	0	0
0	0	0	0
0	0	0	0
0	0	0	0

Fig. 10A

1	1	0	1
1	1	1	0
0	0	0	0
0	0	0	0
0	0	0	0
0	0	0	0

Fig. 10B

1	1	0	1
1	1	1	0
0	0	0	0
0	1	1	0
1	0	0	1
0	0	0	0

Fig. 10C

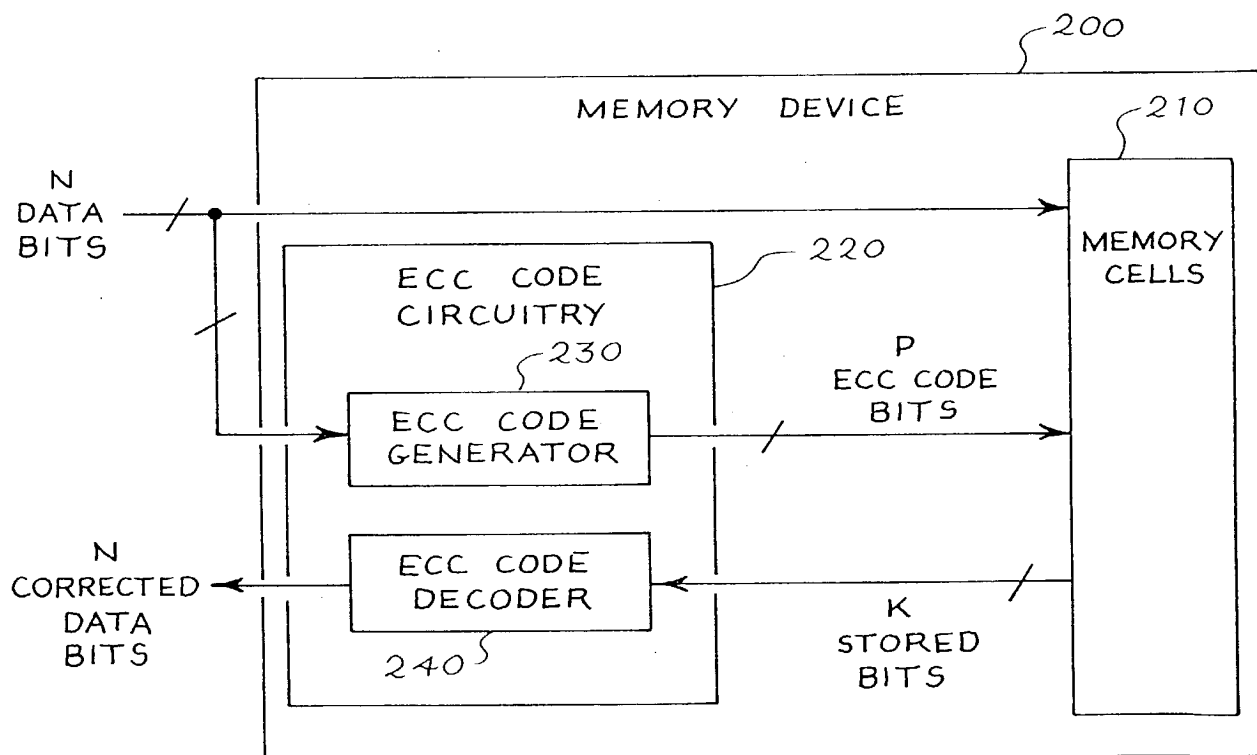


Fig. 11